

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: FUJISAWA et al.
Filed: July 3, 2003
For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
(AS AMENDED)

CLAIM FOR PRIORITY

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 3, 2003

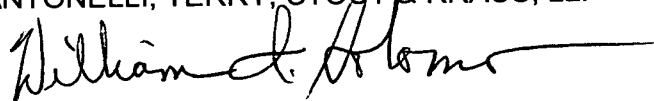
Sir:

Pursuant to 35 USC 119 and 37 CFR 1.55(a), Applicants respectfully claim benefit of the filing date of prior Application No. 9-204,534, filed in Japan on July 30, 1997.

A certified copy of the above-identified Japanese Patent Application was submitted in Application No. 09/126,438, filed July 30, 1998, on July 30, 1998.

Respectfully submitted,

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